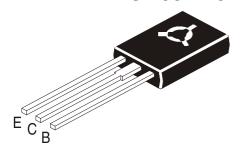






NPN EPITAXIAL SILICON POWER TRANSISTORS



BD135 BD137 BD139

TO126 Plastic Package

Designed for use as Audio Amplifier and Drivers Utilizing

Complementary BD136, BD138, BD140

ABSOLUTE MAXIMUM RATINGS

DESCRIPTION	SYMBOL	BD135	BD137	BD139	UNIT
Collector -Emitter Voltage	V _{CEO}	45	60	80	V
Collector -Emitter Voltage (R _{BE} =1kΩ)	V _{CER}	45	60	100	V
Collector -Base Voltage	V_{CBO}	45	60	100	V
Emitter Base Voltage	V_{EBO}	•	5.0		V
Collector Current	I _C	1.5			
Collector Peak Current	I _{CM}	2.0			
Base Current	I _B	0.5			
Power Dissipation @ T _a =25°C	P _D	1.25			
Derate above 25°C		10			
Power Dissipation @ T _c =25°C	P _D	12.5			
Derate above 25°C		100			mW/ºC
Power Dissipation @ T _c =70°C	P _D	8.0			
Operating And Storage Junction Temperature Range	T _j , T _{stg}	- 55 to +150			

THERMAL CHARACTERISTICS

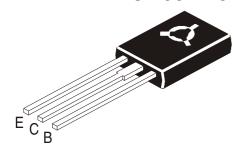
Junction to Ambient in free air	R _{th (j-a)}	100	ºC/W
Junction to Case	R _{th (j-c)}	10	ºC/W

ELECTRICAL CHARACTERISTICS (T_c=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Collector Emitter Sustaining Voltage	*V _{CEO (sus)}	$I_C=30\text{mA}, I_B=0$			
		BD135	45		V
		BD137	60		V
		BD139	80		V
Collector Cut off Current	I _{CBO}	$V_{CB}=30V_{,}I_{E}=0$		0.1	μΑ
		$V_{CB}=30V_{,}I_{E}=0,$ $T_{c}=125^{\circ}C$		10	μΑ
Emitter Cut off Current	I _{EBO}	$V_{EB}=5V$, $I_{C}=0$		10	μΑ
DC Current Gain	*h _{FE}	I_{C} =0.005A, V_{CE} =2V	25		
		I_{C} =0.15A, V_{CE} =2V	40	250	
		$I_C=0.5A, V_{CE}=2V$	25		

^{*}Pulse test:- Pulse width=300µs, duty cycle=2%

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BD135 BD137 BD139

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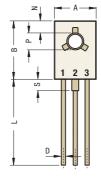
ELECTRICAL CHARACTERISTICS (T_c=25°C unless specified otherwise)

2220 THO AZ OTA HA TO CITE TO CONTROL OF CON						
DESCRIPTION SY		TEST CONDITION	MIN	MAX	UNIT	
DC Current Gain	*h _{FE} Group	$I_{C}=0.15A, V_{CE}=2V$				
		- 6	40	100		
		- 10	63	160		
		- 16	100	250		
		- 25	160	400		
Collector Emitter Saturation Voltage	*V _{CE (sat)}	I _C =0.5A, I _B =0.05A		0.5	V	
Base Emitter On Voltage	*V _{BE(on)}	$^*I_C = 0.5A, V_{CE} = 2V$		1.0	V	

^{*}Pulse test:- Pulse width=300µs, duty cycle=2%

TO126 Plastic Package

T0-126 Leaded Plastic Package





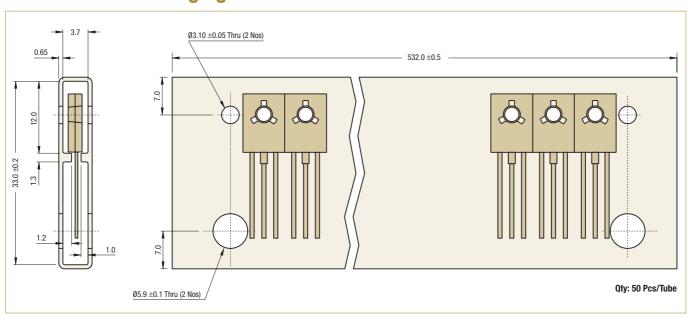
DIM	Min	Max
Α	7.12	8.38
В	10.16	11.43
С	2.29	3.04
D	0.64	0.88
Е	2.040	2.285
F	0.39	0.63

DIM	Min	Max
G	4.07	5.08
L	15.00	16.63
M	0.89	1.65
N	3.31	4.44
Р	2.54	3.30
S	_	2.54

Pin Configurations

Pin 1: Emitter Pin 2: Collector Pin 3: Base

T0-126 Series Packaging Tube



Packaging S	Specificati	ons						
T & A: Tape and Ammo Pack; 1	F & R: Tape and Reel; Bulk	: Loose in Poly Bags; Tube:	Tube and Cartor	n; K: 1 ,000				
Package / Case Type	Packaging Type	Std. Packing	Inner Carton			Outer Carton		
		Qty	Qty	Size L x W x H	Gross Weight	Qty	Size L x W x H	Gross Weight
				(cm)	(Kg)		(cm)	(Kg)
T0-126	Bulk	2,000	2K	19x19x8	1.4	20K	46 x 38 x 22	15.6
	Tube	1,000 (50 pcs/tube)	1K	55 x 8 x 10	1.5	10K	55 x 35 x 27	16.3

BD675_683 Rev_2 101002E

BD135 BD137 BD139

TO126
Plastic Package

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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